

WEST Search History

DATE: Saturday, September 25, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=JPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L17	L16 and sense	5
<input type="checkbox"/>	L16	memory and input\$ adj9 data adj9 (invertor or inverter)	27
		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L15	l14 and input\$ adj9 data adj9 (invertor or inverter)	4
<input type="checkbox"/>	L14	L13 and (antifus\$ or anti-fus\$)	82
<input type="checkbox"/>	L13	L12 and resistance	1284
<input type="checkbox"/>	L12	L11 and bit	2165
<input type="checkbox"/>	L11	L10 and row and column	2225
<input type="checkbox"/>	L10	L9 and (invertor or inverter)	2495
<input type="checkbox"/>	L9	L8 and memory array	3703
<input type="checkbox"/>	L8	L7 and address\$	6802
<input type="checkbox"/>	L7	L6 and (voltage or potential)	VIET Q. NGUYEN PRIMARY EXAMINER
<input type="checkbox"/>	L6	L5 and transistor\$	8086 8138
<input type="checkbox"/>	L5	L4 and sense amplifier\$	9179
<input type="checkbox"/>	L4	L3 and (writ\$ or program\$)	76991
<input type="checkbox"/>	L3	L2 and current	99796
<input type="checkbox"/>	L2	L1 and power adj1 (source or supply)	137340
<input type="checkbox"/>	L1	memory	653067

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